2879
Patent

Docket No.: CDST-C130

Information Disclosure Statement Transmittal

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner of Patents and Trademarks, Washington, D.C., 20231, on the below date of deposit.

Date of Deposit: July 15, 02	Name of Person Making the Deposit: Anthony Chou	Signature of the Person Making the Deposit: <i>Anthony Chou</i>
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): LEE

Serial No.: 09/421,781

Group Art Unit: 2879

Filed: October 19, 1999

Examiner: Sikha Roy

Title: ELECTRODE STRUCTURE AND METHOD FOR FORMING ELECTRODE STRUCTURE FOR A FLAT PANEL DISPLAY

The Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Information Disclosure Statement TransmittalTECHNOLOGY CENTER 2800
JUL 23 2002

RECEIVED

Transmitted herewith is the following:

Formal drawings, totaling sheets.
Informal drawings, totaling sheets.
 Certification for PTO Consideration
 Information Disclosure statement (2 sheet)
Information Disclosure statement and late filing fee
 Form 1449
Petition for Extension of Time
Other:

Fee Calculation (for other than a small entity)

Fee Items				Fee Rate	Total
Petition for Extension of Time (fee calculated elsewhere)				\$.00	
Information Disclosure Statement, late filing				\$240.00	
Other:					00.00
Total Fees					

PAYMENT OF FEES

1. The full fee due in connection with this communication is provided as follows:

The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.

A check in the amount of \$

Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:

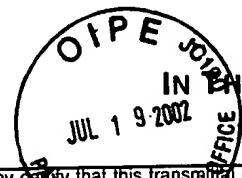
WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date: 7/15/02

By: _____

John P. Wagner, Jr.
Reg. No. 35,398



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No.: CDST-C130

EX
10
7-29-02
J. Actis

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Assistant Commissioner for Patents, Washington, D.C., 20231, on the below date of deposit.			
Date of Deposit:	July 19, 2002	Name of Person Making the Deposit:	Anthony Chou
		Signature of the Person Making the Deposit:	<i>Anthony Chou</i>

Inventor(s): LEE

Serial No.: 09/421,781

Group Art Unit: 2879

Filed: October 19, 1999

Examiner: Sikha Roy

Title: ELECTRODE STRUCTURE AND METHOD FOR FORMING ELECTRODE STRUCTURE FOR A FLAT PANEL DISPLAY

The Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

Certification for PTO Consideration of an Information Disclosure Statement
(Under 37 CFR §1.97)

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JUL 23 2002

TECHNOLOGY CENTER 2800

Consideration of the enclosed Information Disclosure Statement is requested.

1. This certification is being made for this information disclosure statement

accompanying this certification
 Filed _____

2. I hereby certify that:

Each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the statement.
No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart application, or, to the knowledge of the person signing certification after making reasonable inquiry, was known to any individual designated in §1.56(c) more than three months prior to the filing of the statement.

3. The person making this certification is

a person who is substantively involved in the preparation or prosecution of the application, and who is associated with the inventor, with the assignee, or with anyone to whom there is an obligation to assign the application (37 C.F.R. 1.56 (c)) and who signs below.
 the inventor(s) who signs below
 the practitioner who signs below on the basis of the information:
 supplied by the inventors
 supplied by an individual designated in § 1.56(c)
 in the practitioners file

07/31/2002 JARTIS 00000003 P20085 09421781

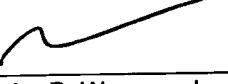
01 FEE126 100.00 CH

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date: 7/15/02

By: 
John P. Wagner, Jr.
Reg. No. 35,398

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: CDST-C130.....

Inventor(s): LEE

Serial No.: 09/421,781

Filed: October 19, 1999

Title: ELECTRODE STRUCTURE AND METHOD FOR FORMING ELECTRODE
STRUCTURE FOR A FLAT PANEL DISPLAY

Group Art Unit: 2879

Examiner: Sikha Roy

TECHNOLOGY CENTER 2800

RECEIVED
JUL 23 2002The Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

<u>Pat. No.</u>	<u>Pat. Title</u>	<u>Grant Date</u>
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Pat. No..	Pat. Title	Grant Date
4,561,009	SEMICONDUCTOR DEVICE	Dec. 24, 1985
4,940,916	ELECTRON SOURCE WITH MICROPOINT EMISSIVE CATHODES AND DISPLAY MEANS BY CATHODOLUMINESCENCE EXCITED BY FIELD EMISSION USING SAID SOURCE	Jul. 10, 1990
5,090,932	METHOD FOR THE FABRICATION OF FIELD EMISSION TYPE SOURCES, AND APPLICATION THEREOF TO THE MAKING OF ARRAYS OF EMITTERS	Feb. 25, 1992
5,191,217	METHOD AND APPARATUS FOR FIELD EMISSION DEVICE ELECTROSTATIC ELECTRON BEAM FOCUSING	Mar. 2, 1993
5,283,500	FLAT PANEL FIELD EMISSION DISPLAY APPARATUS	Feb. 1, 1994
5,319,279	ARRAY OF FIELD EMISSION CATHODES	Jun. 7, 1994
5,509,839	SOFT LUMINESCENT OF FIELD EMISSION DISPLAY	Apr. 23, 1996
5,866,979	METHOD FOR PREVENTING JUNCTION LEAKAGE IN FIELD EMISSION DISPLAYS	Feb. 2, 1999

Pat. No..	Pat. Title	Grant Date
5,894,188	DUAL-LAYER METAL FOR FLAT PANEL DISPLAY	Apr. 13, 1999
5,594,297	FIELD EMISSION DEVICE METALLIZATION INCLUDING TITANIUM TUNGSTEN AND ALUMINUM	Jan. 14, 1997
6,320,138 B1	SUBSTRATE WITH CONDUCTOR FORMED OF LOW-RESISTANCE ALUMINUM ALLOY	Nov. 20, 2001
6,064,149	FIELD EMISSION DEVICE WITH SILICON-CONTAINING ADHESION LAYER	May 16, 2000
6,019,657	DUAL-LAYER METAL FOR FLAT PANEL DISPLAY	Feb. 1, 2000

Please direct all correspondence concerning the above-identified application to the following address:

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 Two North Market Street, Third Floor
 San Jose, California 95113
 (408) 938-9060

Respectfully submitted,

Date: 7/15/02

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